

DATASHEET

# XS5G03-GBO-P Data Card



## 5G NR

Standard	3GPP Release 15
SA Bands	n1/2/3/5/7/8/20/25/28/30/38/40/41/48/66/71/77/78/79
NSA Bands	n1/2/3/5/8/20/28/38/41/66/71/77/78/79
Sub-6 CA	FDD/TDD+FDD/TDD DL CA*
DL 4x4 MIMO	n1/2/3/7/25/30/38/40/41/48/66/77/78/79
UL 2x2 MIMO	n41/77/78/79

## 4G LTE

LTE Category	Cat 12
LTE Bands	B1/2/3/4/5/7/8/12/13/14/17/18/19/20/25/26/28/29/ 30/32/34/38/39/40/41/42/43/46/48/66/71
DL CA	4x4 MIMO, 3CC
UL CA	2CC

## 3G UMTS/WCDMA

Bands	B1/B2/B4/B5/B8
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## GPS

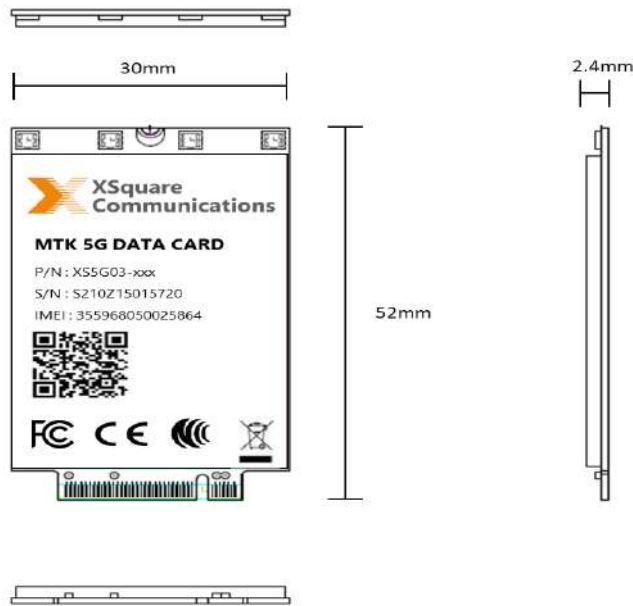
Standard	GPS/GLONASS/BeiDou/Galileo
Frequency Band	L1

## Main Data Through(peak)

5G NR DL/UL	2.77Gbps / 1.25Gbps
4G DL/UL	600Mbps / 150Mbps
3G DL	42.2Mbps (DC-HSPA+)
3G UL	11.5Mbps

## Certifications

Standard	FCC / CE / JRF&JPA / NCC
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## HardWare

Package	M.2 module (key B)
SOC	MediaTek T700 series (MT6880VT) / Integrates ARM Corex-A55
Flash	MCP 4Gb+4Gb
Power Supply	3.135V-3.63V, typical: 3.3V
Interfaces	PCIE-e 3.0(2.0) / SIM GPIO (Reset / Power / LED ) / UART
RF Antenna port	ANT0: 617 MHz ~ 5000 MHz ANT1: 1805 MHz ~ 5000 MHz ANT2: 1710 MHz ~ 5000 MHz ANT3: 617 MHz ~ 5000 MHz
Dimensions	52mm x 30mm x 2.4mm (Max)
Weight	8.7g
Operation Temp	-30 ~ +70 °C
Storage Temp	-40 ~ +90 °C

## SoftWare

Update / Management.	Firmware update via PCIe
OS Version.	TBD (Depends on host OS) Module support from Linux 4.19
Drivers	PCIe 3.0 1L



Pin define				
Pin No.	Pin Name		Pin No.	Pin Name
74	+3.3V		75	CONFIG_2 (GND)
72	+3.3V		73	VIO_CFG (NC)
70	+3.3V		71	GND
68	NC		69	CONFIG_1 (GND)
66	SIM Detect (I) (1.8V)		67	RESET# (I) (1.8V)
64	COEX1 (IO) (1.8V)		65	ANTCTL3 (O) (1.8V)
62	COEX2 (IO) (1.8V)		63	ANTCTL2 (O) (1.8V)
60	COEX3 (IO) (1.8V)		61	ANTCTL1 (O) (1.8V)
58	MIPI2_D_SDATA (IO) (1.8V)		59	ANTCTL0 (O) (1.8V)
56	MIPI2_D_SCLK (O) (1.8V)		57	GND
54	PEWake# (IO) (3.3/1.8V)		55	REFCLKP
52	CLKREQ# (IO) (3.3/1.8V)		53	REFCLKN
50	PERST# (IO) (3.3/1.8V)		51	GND
48	NC		49	PERp0
46	NC		47	PERn0
44	I2C_IRQ# (I) (1.8V)		45	GND
42	I2C_SDA (IO) (1.8V)		43	PETp0
40	I2C_SCL (O) (1.8V)		41	PETn0
38	NC		39	GND
36	USIM1_PWR (O)		37	USB3.0_RX+
34	USIM1_DATA (IO)		35	USB3.0_RX-
32	USIM1_CLK (O)		33	GND
30	USIM1_RESET (O)		31	USB3.0_TX+
28	I2S0_LRCK (IO) (1.8V)		29	USB3.0_TX-
26	W_DISABLE2# (I) (3.3/1.8V)		27	GND
24	I2S0_DO (O) (1.8V)		25	DPR (I) (1.8V)
22	I2S0_DI (I) (1.8V)		23	WOWWAN# (IO) (1.8V)
20	I2S0_BCK (IO) (1.8V)		21	CONFIG_0 (GND)
	Notch			Notch
	Notch			Notch
	Notch			Notch
	Notch			Notch
10	LED1# (O) (3.3V)		11	GND
8	W_DISABLE1# (I) (3.3/1.8V)		9	USB_D-
6	FULL_CARD_POWER_OFF# (I) (3.3/1.8V)		7	USB_D+
4	+3.3V		5	GND
2	+3.3V		3	GND
			1	CONFIG_3 (NC)

